

## EAST Search History

| Ref # | Hits | Search Query  | DBs                                    | Default Operator | Plurals | Time Stamp       |
|-------|------|---|--|------------------|---------|------------------|
| L1    | 311  | 703/24.ccls.  | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/09/15 14:47 |
| L4    | 806  | trace\$1 and packet\$4 and compress\$4 and (emulation or debug) and ((integrated adj circuit) or processor or chip) | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/09/15 15:56 |
| L5    | 158  | swoboda.in. and gary.in.  | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/09/15 16:11 |
| L7    | 15   | L5 and (emulation and sequence and process\$3).clm.   | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/09/15 16:29 |
| L8    | 477  | 714/45.ccls.  | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/09/15 16:57 |
| L9    | 360  | 703/26.ccls.  | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/09/15 16:57 |

## EAST Search History

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| L1    | 80   | (emulation and sequence and process\$3).clm. | US-PGPUB                               | OR               | OFF     | 2006/09/15 17:48 |
| L3    | 3    | "6912675".pn.                                | US-PGPUB;<br>USPAT;<br>EPO;<br>DERWENT | OR               | OFF     | 2006/09/15 18:00 |

Search Results

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((trace <near> compress\*<and>debug\*)) <and> (pyr >= 1951 <and> pyr <= 2001)

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## » Key



Indicates full text access

|          |                            |
|----------|----------------------------|
| IEEE JNL | IEEE Journal or Magazine   |
| IEE JNL  | IEE Journal or Magazine    |
| IEEE CNF | IEEE Conference Proceeding |
| IEE CNF  | IEE Conference Proceeding  |
| IEEE STD | IEEE Standard              |

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- ☐ **1. Efficient program tracing**  
 Larus, J.R.;  
Computer  
 Volume 26, Issue 5, May 1993 Page(s):52 - 61  
 Digital Object Identifier 10.1109/2.211900  
[Abstract](#) | Full Text: [PDF\(1732 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
- ☐ **2. Environment for PowerPC microarchitecture exploration**  
 Moudgill, M.; Wellman, J.-D.; Moreno, J.H.;  
Micro, IEEE  
 Volume 19, Issue 3, May-June 1999 Page(s):15 - 25  
 Digital Object Identifier 10.1109/40.768496  
[Abstract](#) | Full Text: [PDF\(1948 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
- ☐ **3. Subject Index**  
Computers, IEEE Transactions on  
 Volume 50, Issue 12, Dec. 2001 Page(s):1380 - 1388  
 Digital Object Identifier 10.1109/TC.2001.970577  
[Abstract](#) | Full Text: [PDF\(70 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
- ☐ **4. Exploiting image processing locality in cache pre-fetching**  
 Cucchiara, R.; Piccardi, M.;  
High Performance Computing, 1998. HIPC '98. 5th International Conference On  
 17-20 Dec. 1998 Page(s):466 - 472  
 Digital Object Identifier 10.1109/HIPC.1998.738023  
[Abstract](#) | Full Text: [PDF\(124 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ **5. The Motorola PowerPC™ PEEK profiler**  
 Stewart, K.; Butt, F.; Sarkisian, D.; Breternitz, M., Jr.;  
Performance, Computing, and Communications Conference, 1997. IPCCC 1997., IEEE International  
 5-7 Feb. 1997 Page(s):342 - 349  
 Digital Object Identifier 10.1109/PCCC.1997.581537  
[Abstract](#) | Full Text: [PDF\(824 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ **6. Visualizing Hilbert curves**  
 Max, N.;  
Visualization '98. Proceedings

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## Modify Search

(trace <near> compress\*) <and> (pyr >= 1951 <and> pyr <= 2001)

**Search** 

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Display Format: ☒ Citation ☐ Citation & Abstract

## » Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ **1. Accurate low-cost methods for performance evaluation of cache memory systems**  
Laha, S.; Patel, J.H.; Iyer, R.K.;  
[Computers, IEEE Transactions on](#)  
Volume 37, Issue 11, Nov. 1988 Page(s):1325 - 1336  
Digital Object Identifier 10.1109/12.8699  
[AbstractPlus](#) | Full Text: [PDF](#)(944 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ **2. Address tracing for parallel machines**  
Stunkel, C.B.; Janssens, B.; Fuchs, W.K.;  
[Computer](#)  
Volume 24, Issue 1, Jan. 1991 Page(s):31 - 38  
Digital Object Identifier 10.1109/2.67191  
[AbstractPlus](#) | Full Text: [PDF](#)(628 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ **3. Efficient program tracing**  
Larus, J.R.;  
[Computer](#)  
Volume 26, Issue 5, May 1993 Page(s):52 - 61  
Digital Object Identifier 10.1109/2.211900  
[AbstractPlus](#) | Full Text: [PDF](#)(1732 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ **4. Stack evaluation of arbitrary set-associative multiprocessor caches**  
Yuguang Wu; Muntz, R.;  
[Parallel and Distributed Systems, IEEE Transactions on](#)  
Volume 6, Issue 9, Sept. 1995 Page(s):930 - 942  
Digital Object Identifier 10.1109/71.466631  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(1240 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ **5. Locality as a visualization tool**  
Grimsrud, K.; Archibald, J.; Frost, R.; Nelson, B.;  
[Computers, IEEE Transactions on](#)  
Volume 45, Issue 11, Nov. 1996 Page(s):1319 - 1326  
Digital Object Identifier 10.1109/12.544490  
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(912 KB) IEEE JNL  
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- ☐ **6. Compression-based program characterization for improving cache memory performance**  
Phalke, V.; Gopinath, B.;  
[Computers, IEEE Transactions on](#)

All Results

[E Johnson](#)

[D Knuth](#)

[R Fork](#)

[J Ha](#)

[C Cruz](#)

**PDATS Lossless Address Trace Compression For Reducing File Size And Access Time - group of 3 »**

EE Johnson, J Ha - Computers and Communications, 1994. IEEE 13th Annual ..., 1994 - [ieeexplore.ieee.org](#)  
Page 1 0-7803-1814-5/94 \$4.00 © 1994 IEEE 213 PDATS Lossless Address **Trace Compression**  
For Reducing File Size And Access Time Eric E. Johnson and Jiheng Ha ...  
[Cited by 45](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

**Address trace compression through loop detection and reduction - group of 2 »**

EN Elnozahy - ACM SIGMETRICS Performance Evaluation Review, 1999 - [portal.acm.org](#)  
Page 1. Address **Trace Compression** Through Loop Detection and Reduction EN  
Elnozahy IBM Austin Research Lab 11400 Burnet Rd. Austin ...  
[Cited by 16](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

**Lossless Trace Compression - group of 3 »**

EE Johnson, J Ha, MB Zaidi - IEEE Transactions on Computers, 2001 - [csdl.computer.org](#)  
... In this paper, we discuss a range of information-lossless address and instruction  
**trace compression** schemes that can reduce both storage space and access time ...  
[Cited by 14](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

**PDATS II: improved compression of address traces - group of 4 »**

EE Johnson - Performance, Computing and Communications Conference, 1999. ..., 1999 - [ieeexplore.ieee.org](#)  
... The PDATS family of **trace compression** techniques achieves **trace** coding densities  
of about six references per byte - ... 2. PDATS address **trace compression** ...  
[Cited by 9](#) - [Related Articles](#) - [Web Search](#)

**Dynamic Huffman coding - group of 2 »**

DE Knuth - Journal of Algorithms, 1985 - [portal.acm.org](#)  
... Martin Burtscher, VPC3: a fast and effective **trace-compression** algorithm, ACM  
SIGMETRICS Performance Evaluation Review, v.32 n.1, June 2004. ...  
[Cited by 129](#) - [Related Articles](#) - [Web Search](#)

**Compression of optical pulses to six femtoseconds by using cubic phase compensation - group of 5 »**

RL Fork, CHB Cruz, PC Becker, CV Shank - Opt. Lett, 1987 - OSA  
... in the same paper that the principal remaining problem in pulse **compression** of large ...  
position of the peak relative to the horizontal axis for each **trace** is a ...  
[Cited by 327](#) - [Related Articles](#) - [Web Search](#)

**Mache: no-loss trace compaction - group of 3 »**

AD Samples - ACM SIGMETRICS Performance Evaluation Review, 1989 - [portal.acm.org](#)  
... This technique is unlike previously reported **trace compression** techniques in that  
it compresses without loss of information and, therefore, does not affect ...  
[Cited by 54](#) - [Related Articles](#) - [Web Search](#) - [Library Search](#)

**Abstract execution: a technique for efficiently tracing programs - group of 2 »**

JR Larus - Software—Practice & Experience, 1990 - [portal.acm.org](#)  
... Martin Burtscher, VPC3: a fast and effective **trace-compression** algorithm, ACM  
SIGMETRICS Performance Evaluation Review, v.32 n.1, June 2004. ...  
[Cited by 100](#) - [Related Articles](#) - [Web Search](#) - [Library Search](#)

**Compression of high-energy laser pulses below 5 fs - group of 8 »**

M Nisoli, S De Silvestri, O Svelto, R Szipocs, K ... - Opt. Lett, 1997 - OSA  
... 7 By best **compression** of the pulse whose spectrum is shown in Fig. 2(a), we

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Results 1 - 10 of about 845 for **trace compression debug**. (0.12 seconds)

[Abstract execution: a technique for efficiently tracing programs - group of 2 »](#)

JR Larus - Software—Practice & Experience, 1990 - portal.acm.org

... Ramakrishnan Rajamony, Alan L. Cox, Performance **debugging** shared memory parallel ...

Martin Burtscher, VPC3: a fast and effective **trace-compression** algorithm, ACM ...

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[\[PS\] Developing Monitoring and Debugging Tools for the AP1000 Array Multiprocessor - group of 4 »](#)

CW Johnson, PB Thistlewaite, D Walsh, M Zellner - Proceedings of the Second Fujitsu-ANU CAP Workshop, RP Brent ..., 1991 - cs.anu.edu.au

... in the extended **trace** format, which includes a ... The replay **debugger** allows the programmer to investigate ... display-variables level of **debugging**, while simulating ...

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[Trace-driven memory simulation: a survey - group of 11 »](#)

RA Uhlig, TN Mudge - ACM Computing Surveys (CSUR), 1997 - portal.acm.org

Page 1. **Trace-Driven Memory Simulation: A Survey** ... Although conceptually simple, a number of factors make **trace-driven** simulation difficult in practice. ...

[Cited by 157](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Efficient Tracing for On-the-Fly Space-Time Displays in a Debugger for Message Passing Programs - group of 4 »](#)

R Hood, G Matthews - Proceedings of the 1st International Symposium on Cluster ..., 2001 - doi.ieeecomputersociety.org

... message passing library, and gdb **debugger** process controlling it through which p2d2

**debugging** commands are issued [5]. The **trace compression** described in ...

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[Event and state-based debugging in TAU: a prototype - group of 6 »](#)

S Shende, J Cuny, L Hansen, J Kundu, S McLaughry, ... - Proceedings of the SIGMETRICS symposium on Parallel and ..., 1996 - portal.acm.org

... the use of Ariadne and the extended modeling language, we **debug** a parallel version

of an ... **compression**. ... user-defined events USERMERGE and USERNOMERGE to **trace** ...

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[Efficient tracing for on-the-fly space-time displays in a debuggerfor message passing programs](#)

R Hood, G Matthews - Cluster Computing and the Grid, 2001. Proceedings. First ..., 2001 - ieeexplore.ieee.org

... message passing library, and gdb **debugger** process -controlling it through which

p2d2 **debugging** commands are issued [SI. The **trace compression** described in ...

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[\[PS\] Debugging Haskell by observing intermediate data structures - group of 6 »](#)

A Gill - Electronic Notes in Theoretical Computer Science, 2000 - cse.ogi.edu

... A stack **trace** becomes a parent tree. ... Should the **debugger** do extra evaluations? ... This argu- ment can be considered a generalization of the "**debugging** via dataflow ...

[Cited by 43](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

[\[BOOK\] Input/output behavior of supercomputing applications - group of 18 »](#)

EL Miller, RH Katz - 1991 - ACM Press New York, NY, USA

... on the Cray Y-MP. We chose to **trace** applications with high I/O rates, both in megabytes per second and accesses per second. While ...

[Cited by 71](#) - [Related Articles](#) - [Web Search](#) - [Library Search](#)

[Cyclic Debugging Using Execution Replay - group of 5 »](#)

M Ronsse, M Christiaens, K De Bosschere - Proceedings of the International Conference on Computational ..., 2001 - Springer

... If one wants to **debug** such a program, it is sufficient ... allows for the use of a simple **compression** scheme [RLB95] which can further reduce the **trace** files ...

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All Results

J Larus

R Uhlig

T Mudge

S Shende

T Chilimbi

**Abstract execution: a technique for efficiently tracing programs - group of 2 »**

JR Larus - Software—Practice & Experience, 1990 - portal.acm.org

... Ramakrishnan Rajamony, Alan L. Cox, Performance **debugging** shared memory parallel ...

Martin Burtcher, VPC3: a fast and effective **trace-compression** algorithm, ACM ...

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**[PS] Developing Monitoring and Debugging Tools for the AP1000 Array Multiprocessor - group of 4 »**

CW Johnson, PB Thistlewaite, D Walsh, M Zellner - Proceedings of the Second Fujitsu-ANU CAP Workshop, RP Brent ..., 1991 - cs.anu.edu.au

... Two variants of LERP **trace** format allow complete traces (including message contents - allowing process replay and detailed **debugging**) and abbreviated traces ...

Cited by 2 - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

**Designing a trace format for heap allocation events - group of 10 »**

T Chilimbi, R Jones, B Zorn - ACM SIGPLAN Notices, 2001 - portal.acm.org

... Further, separation and **compression** of the address stream ... compressing different streams of a **trace** is directly ... a part of the heap), **debugging**, profiling and so ...

Cited by 12 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

**Trace-driven memory simulation: a survey - group of 11 »**

RA Uhlig, TN Mudge - ACM Computing Surveys (CSUR), 1997 - portal.acm.org

Page 1. **Trace-Driven Memory Simulation: A Survey** ... Although conceptually simple, a number of factors make **trace-driven** simulation difficult in practice. ...

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**Efficient Tracing for On-the-Fly Space-Time Displays in a Debugger for Message Passing Programs - group of 4 »**

R Hood, G Matthews - Proceedings of the 1st International Symposium on Cluster ..., 2001 - doi.ieeecomputersociety.org

... Software developers who need to **debug** message- passing programs ... it through which p2d2 **debugging** commands are issued [5]. The **trace compression** described in ...

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**Event and state-based debugging in TAU: a prototype - group of 6 »**

S Shende, J Cuny, L Hansen, J Kundu, S McLaughry, ... - Proceedings of the SIGMETRICS symposium on Parallel and ..., 1996 - portal.acm.org

... its **compression**. To begin **debugging** this program with Ariadne, we added user-defined events USERMERGE and USERNOMERGE to **trace** the ...

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**Efficient tracing for on-the-fly space-time displays in a debugger for message passing programs**

R Hood, G Matthews - Cluster Computing and the Grid, 2001. Proceedings. First ..., 2001 - ieeexplore.ieee.org

... Software developers who need to **debug** message- passing ... it through which p2d2 **debugging** commands are ... The **trace compression** described in section 2.3 is performed ...

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**[PS] Debugging Haskell by observing intermediate data structures - group of 6 »**

A Gill - Electronic Notes in Theoretical Computer Science, 2000 - cse.ogi.edu

... **debugging** an imperative program using traditional **debug-** ging technology ... to provide the user with **debugging** facilities do ... A stack **trace** becomes a parent tree. ...

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